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DB = PGPB, USPT, USOC, EPAB, JPAB, DWPI, TDBD; PLUR = YES; OP = OR			
	L18	L17 and l16 and l8	.9
	L17	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) and ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) and (serial near2 parallel near3 convert\$3)	662
	L16	332/106.ccls. or 332/107.ccls. or 15	4142
	L15	(332/106.ccls. or 332/107.ccls. or 15)and 113	0
	L14	332/106.ccls. or 332/107.ccls. or 15 and 113	344
	L13	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) and ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) and (trellis near2 (cod\$3 or modulat\$3)) and (serial near2 parallel near3 convert\$3)	28
	L12	(trellis near2 (cod\$3 or modulat\$3) or (bit near2 mapp\$3)) same ((pre adj distortion\$) or predistortion)	5
	L11	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) and ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) and (trellis near2 (cod\$3 or modulat\$3)) and ((pre adj distortion\$) or predistortion)	19
	L10	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) same ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) same (trellis near2 (cod\$3 or modulat\$3))	11
	L9	L6 and L8	164
	L8	@ad<=20030714	24484021
	L7	@ad<=-20030714	0
	L6	L5 and L4	215
	L5	(375/295).ccls. or (375/296).ccls. or (375/297).ccls. or (332/117).ccls. or (332/123).ccls. or (332/124).ccls. or (332/125).ccls.	3850
	L4	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) same ((control\$3 or adjust\$3 or fix\$3) near2 phase\$)	20004
	L3	((adjust\$3 or control\$3 or fix\$3) near2 (gain or amplitude or power\$)) same ((control\$3 or adjust\$3 or fix\$3) near2 phase\$) same (convert\$3 near3 (serial near2 parallel))	6
	L2	(control\$3 near2 (gain or amplitude or power\$)) same (control\$3 near2 phase\$) same (convert\$3 near3 (serial near2 parallel))	5
	L1	(predistor\$3) same (control\$3 near2 (gain or amplitude or power\$)) same (control\$3 near2 phase\$) same (convert\$3 near3 (serial near2 parallel))	0

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Wed, 2 May 2007, 6:12:11 PM EST

Search Query Display



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#1 ((((predistor*) and (multi <near/2> bit* <near/2> (buffer* <or> memor*)))<in>metadata)

#2 ((((predistor*) <and> (multi* <near/2> bit* <near/2> (buffer* <or> memor*)))<in>metadata)

#3 (((predistor*) <and> (serial <near/3> parallel))<in>metadata)

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